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IT0020-CIP

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

ZI-PING CHEN ET AL.

Serial No.: 10/726,490

Filed: December 4, 2003

For: SILICON-CONTROLLED
RECTIFIER WITH DYNAMIC
HOLDING VOLTAGE FOR ON-
CHIP ELECTROSTATIC
DISCHARGE PROTECTION

Art Unit: 2836

Examiner: SIRCUS, Brian

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants wish to make of record in the above-identified application the document or documents referenced on the attached Form PTO-1449. A copy of each reference is enclosed herewith.

The undersigned believes that this Information Disclosure Statement is being filed before the mailing date of a first Office Action on the merits for the above-referenced application. Accordingly, Applicants do not believe that a fee is due for filing this paper. However, should a first action on the merits have been issued on the same day or before this Information Disclosure Statement is filed, please accept this Information Disclosure Statement under Rule 97(c) and charge the requisite Rule 17(p) fee to our Deposit Account No. 50-1390, under Order No. IT0020-CIP and proceed to consider this Information Disclosure Statement.

It is respectfully requested that the information be expressly considered during the prosecution of this application, and that each reference be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This submission does not represent that any referenced document is material or constitutes "prior art." If it should be determined that one or more of the referenced documents constitute "prior art" under United States law, Applicants reserve the right to present to the Office the relevant facts and law regarding the appropriate status of the reference or references.

Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over any referenced document, should it be applied against the claims of the present application.

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Date: October 26, 2004

Respectfully submitted,

ZI-PING CHEN ET AL.

By:



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PTO/SB/08B (10-01)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Substitute for form 1449B/PTO		Complete if Known	
		Application Number	10/726,490
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Filing Date	December 4, 2003
		First Named Inventor	Zi-Ping Chen et al.
		Group Art Unit	2836
		Examiner Name	SIRCUS, Brian
		Attorney Docket Number	IT0020-CIP
Sheet	2	of	2

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		M.D. Ker, "Lateral SCR Devices with Low-Voltage High-Current Triggering Characteristics for Output ESD Protection in Submicrom CMOS Technology", IEEE Trans. Electron Devices, Vol 45, pp. 849-860, April, 1998	<input type="checkbox"/>
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Examiner Signature		Date Considered	
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* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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